

Jr. Digital Design Engineer

Job Summary:

A Jr. Digital Design Engineer assists in the development, design, and verification of digital and mixed-signal ICs utilizing leading edge technologies with industry standard ASIC tools. Products to be designed/verified may include power management, signal management and mixed signal functions.

MPS products include: switching regulators, sensors, motor control, display drivers, audio amplifiers and power management ICs for fast-growing portable and non-portable markets such as notebooks, cell phones, telecom, digital camera, automobile and network equipment.

Essential Functions:

- Contributes to the design, development, & verification of digital / mixed-signal IC's.
- Works closely under the supervision of Senior members of IC/AE team.
- Digital Design, Behavioral modeling, and Digital Verification/testcases using standard RTL/DV languages (VHDL, Verilog, SysVerilog, UVM).
- Knowledge & Use of industry standard ASIC tools/flow for daily work: Digital Simulators, synthesis tools, DFT, LEC, STA, etc.

Qualifications:

- Bachelor/MS in Electrical Engineering with emphasis in Digital Design/VLSI coursework.
- Has the ability to follow instructions/tasks according to design specifications/procedures.
- Basic knowledge of ASIC development process and digital design techniques.
- Knowledge of standard DV languages (SystemVerilog, UVM) and in behavioral/RTL coding.
- Executing tasks that hit project milestone.
- Good written/verbal communication skills and strong team work/collaboration.
- Knowledge/Experience with the following is a plus:
 - Embedded designs and/or firmware development
 - Knowledge of power management industry/applications
 - I2C, SPI, USB, PMBUS
 - Scripting and automation languages like TCL, Python and C/C++.

Location: Barcelona, Spain

Please apply online: <https://www.monolithicpower.com/about-mps/careers/job-listings.html>